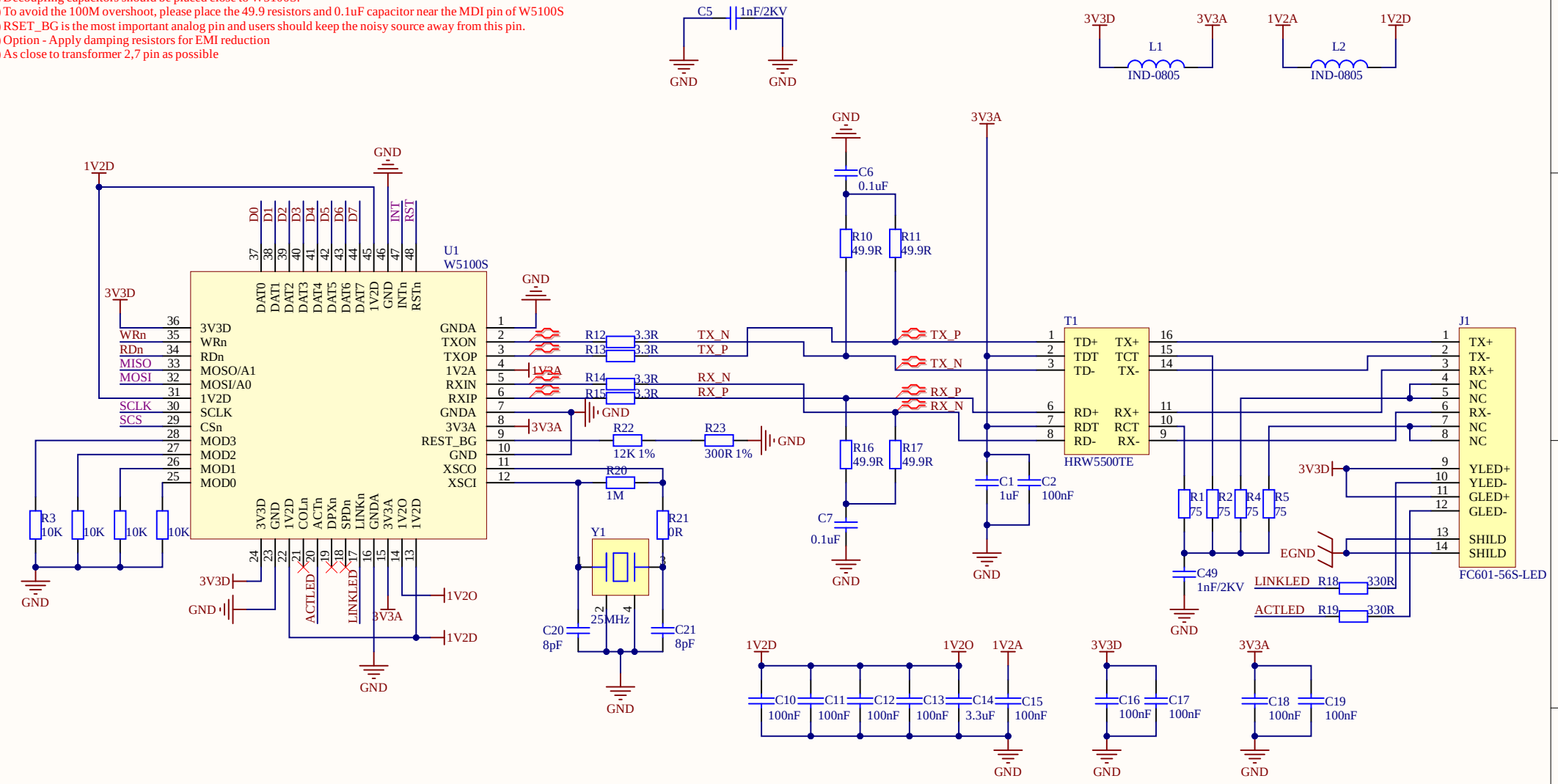
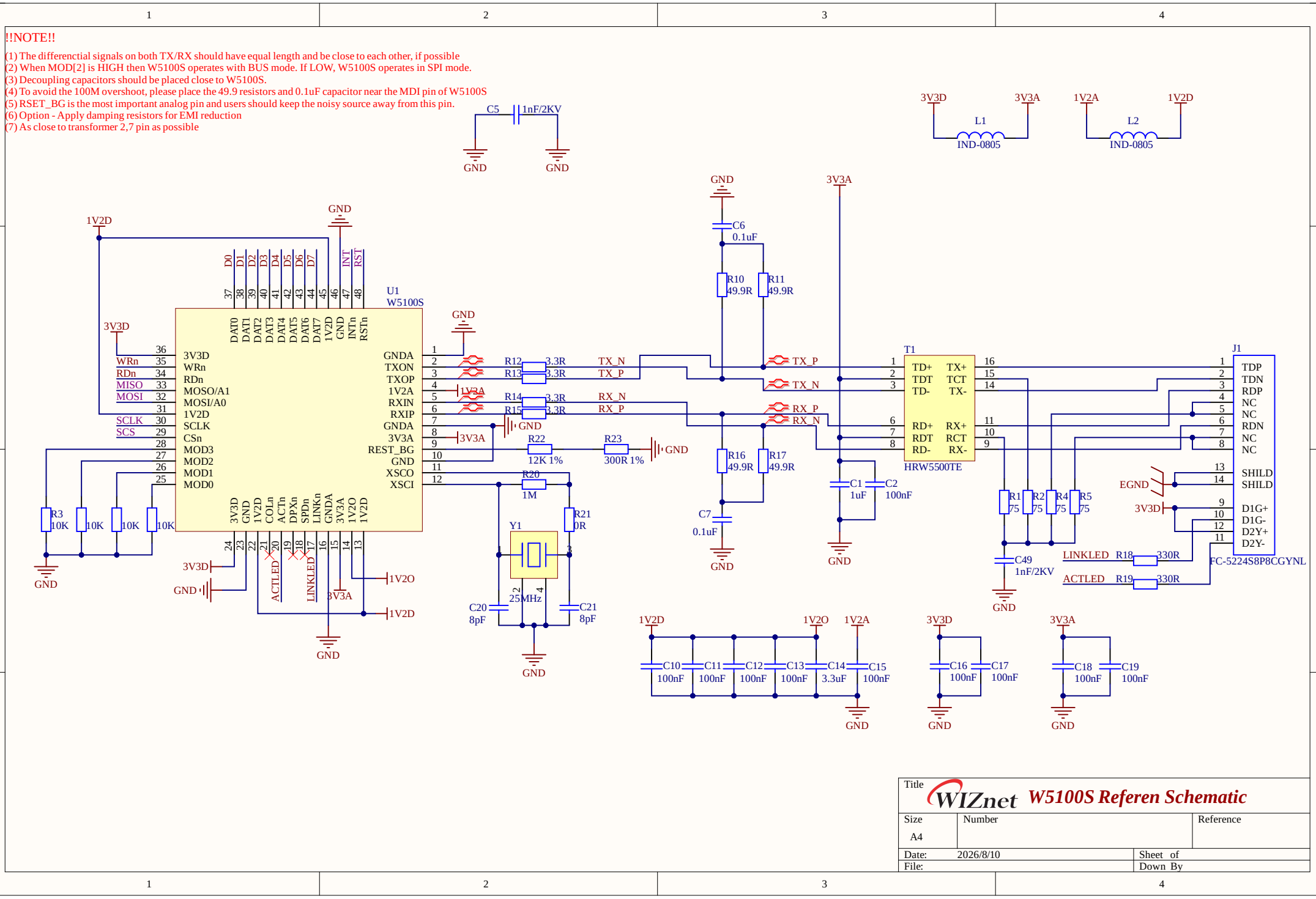
!!NOTE!!

(1) The differenctial signals on both TX/RX should have equal length and be close to each other, if possible
(2) When MOD[2] is HIGH then W5100S operates with BUS mode. If LOW, W5100S operates in SPI mode.
(3) Decoupling capacitors should be placed close to W5100S.
(4) To avoid the 100M overshoot, please place the 49.9 resistors and 0.1uF capacitor near the MDI pin of W5100S
(5) RSET_BG is the most important analog pin and users should keep the noisy source away from this pin.
(6) Option - Apply damping resistors for EMI reduction
(7) As close to transformer 2,7 pin as possible

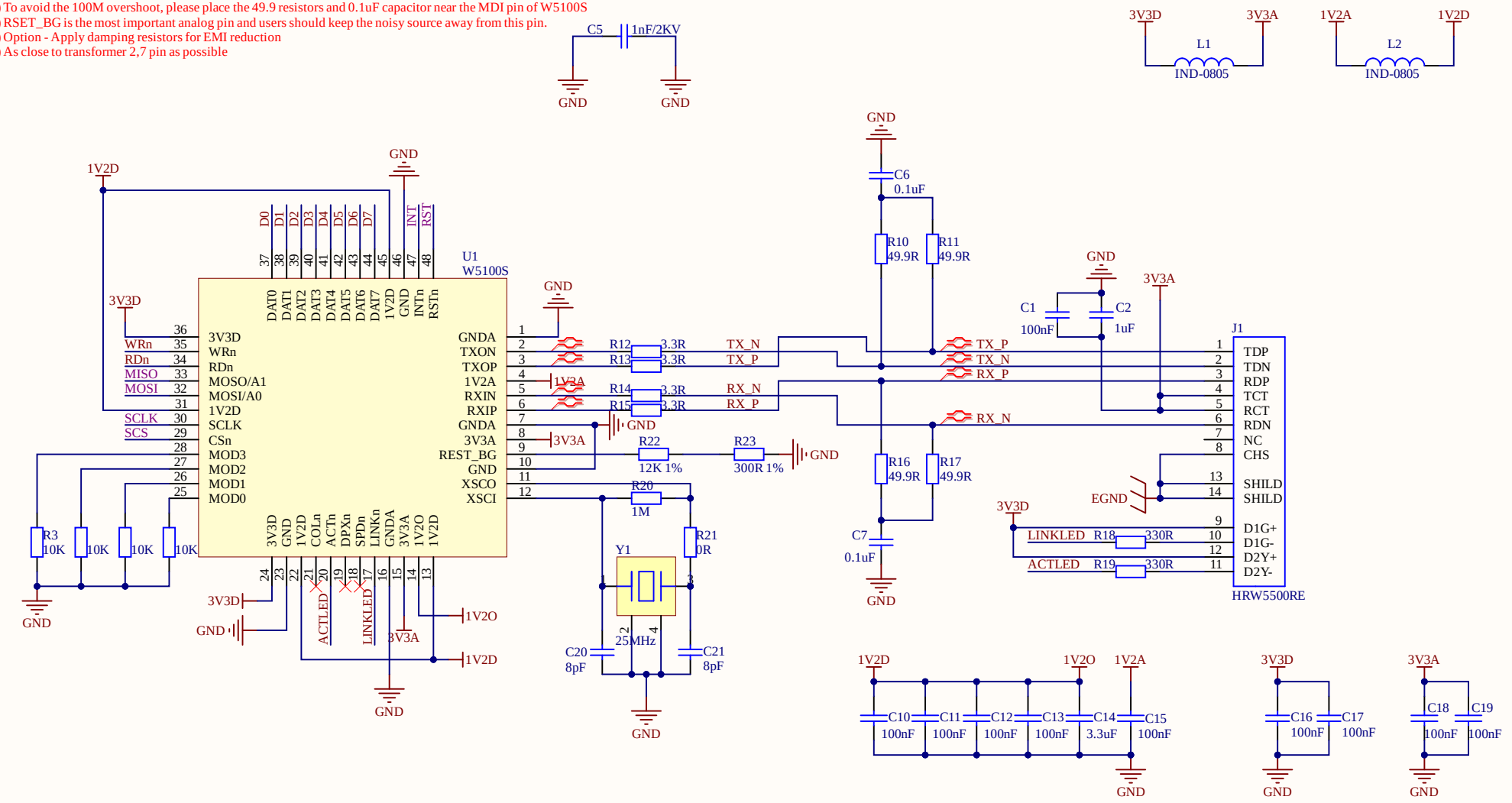


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C5 1nF/2KV
L1 IND-0805
L2 IND-0805
U1 W5100S
3V3D
3V3A
1V2D
1V2A
1V2O
GND
C6 0.1uF
R10 49.9R
R11 49.9R
R12 3.3R
R13 3.3R
R14 3.3R
R15 3.3R
TX_N
TX_P
RX_N
RX_P
R16 49.9R
R17 49.9R
C7 0.1uF
C1 1uF
C2 100nF
T1 HRW5500TE
R1 75
R2 75
R4 75
R5 75
C49 1nF/2KV
J1 FC-5224S8P8CGYNL
EGND
LINKLED R18 330R
ACTLED R19 330R
R22 12K 1%
R23 300R 1%
R20 1M
R21 0R
Y1 25MHz
C20 8pF
C21 8pF
R3 10K
C10 100nF
C11 100nF
C12 100nF
C13 100nF
C14 3.3uF
C15 100nF
C16 100nF
C17 100nF
C18 100nF
C19 100nF
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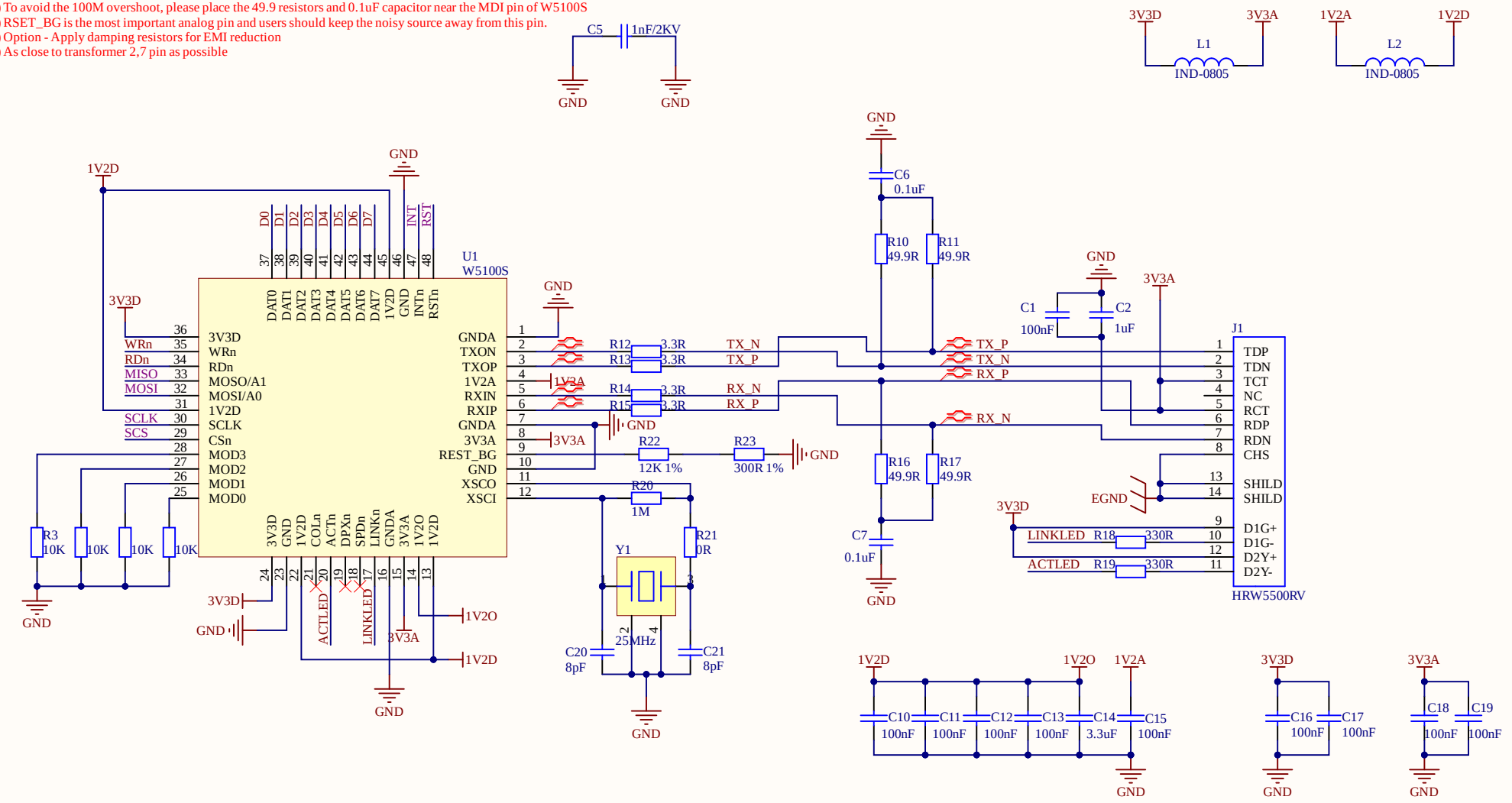
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